XJTAG Offers Boundary Scan Workshop in UK

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COVENTRY, ENGLAND – **XJTAG** is running a free boundary scan training workshop here June 12.

The workshop will provide an introduction to boundary scan and show how this debug, test and programming process can be used throughout the product lifecycle.

XJTAG's will explain how boundary scan can be used from start to finish to improve designs and reduce re-spins, and to enhance test coverage, fault diagnosis and production yields on complex BGA-populated circuits.

An overview of IEEE 1149.x will be provided, as well as a demonstration of how to communicate with the JTAG chain and how to interact with JTAG devices such as FPGAs. The firm will provide an introduction to board testing using the JTAG chain, explain how to describe a circuit to enable JTAG testing and how to run an infrastructure connection test. The presenters will outline how to test non-JTAG elements of a board design using boundary scan.

To register, visit www.xjtag.com/workshop.php .

For more information, contact info@xjtag.com .