

## Webinar: Optimize System Performance with FPGA/PCB Co-Design

Written by

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**ATLANTA – UP Media Group** next month will present a free webinar on optimizing system performance with FPGA/PCB co-design.

The webinar takes place June 3 at 2 pm Eastern.

Bob Potock, director of FPGA marketing for **Mentor Graphics'** Systems Design Division, is the presenter.

The third in a series, this event will discuss elements of an effective FPGA on-board strategy, specific implementation options, and how to boost productivity and PCB quality. The webinar will address effective integration of multiple FPGAs on a PCB. It will focus on new FPGA/PCB concurrent design processes and tools said to significantly improve system performance, product costs and productivity.

UPMG is the parent company of *Circuits Assembly* and *Printed Circuit Design & Fab*.

To register, click here: [FPGA Webinar](#) .